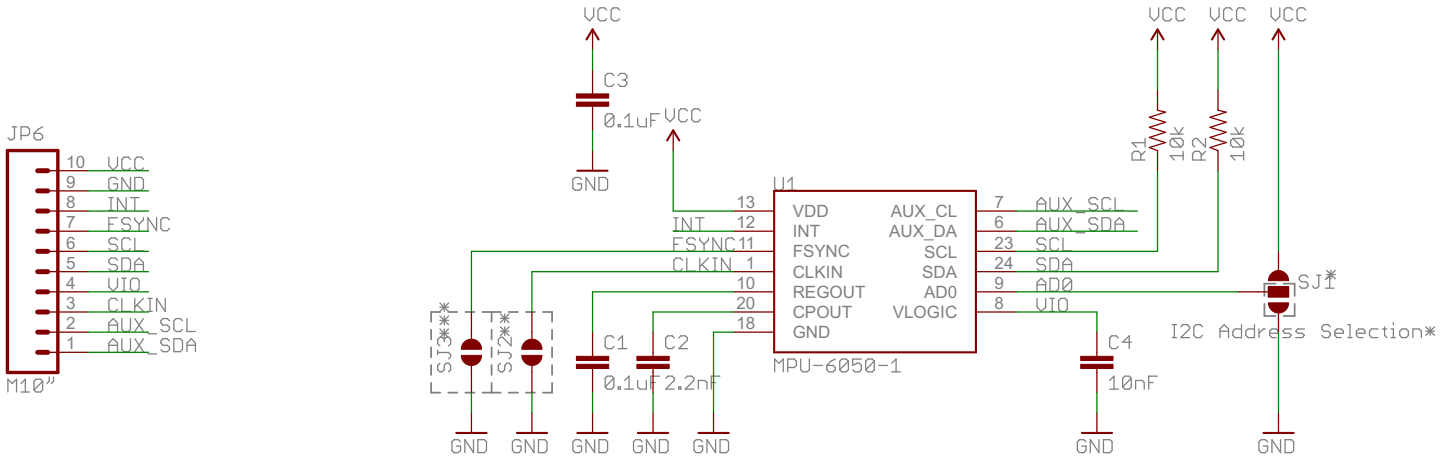

* Two MPU-60X0s can be connected to the same I2C bus
The LSB bit of the 7 bit address is determined by the logic level on pin AD0.
Default Address = 0x68 (pin AD0 is logic low)
Alternative Address =0x69 (pin AD0 is logic high)

** Optional external reference clock input. Connected to GND by default.
Cut trace for external clock

*** Frame synchronization digital input. Connected to GND by default
Cut trace for external sync

Released under the Creative Commons Attribution Share-Alike 3.0 License
http://creativecommons.org/licenses/by-sa/3.0
Design by: Joel Bartlett

open hardware

TITLE: MPU-6050_Breakout V11	
Document Number:	REV:
Date: not saved!	Sheet: 1/1